

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re U.S. Patent Application of:

INVENTORS: Hooli et al.

SERIAL NO.: 10/783,049

ART UNIT:

FILED: 2/19/04

**EXAMINER:** 

TITLE: Chip-Level or Symbol-Level Equalizer Structure for Multiple Transmit

And Receiver Antenna Configuration"

ATTORNEY DOCKET NO.: 872.0178.U1(US)

Commissioner For Trademarks P.O. Box 1450 Alexandria, VA 22313-1450

## SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

## Dear Sir:

Pursuant to Sections 609 and 707.05(b) of the MPEP and 37 CFR 1.97-1.99, a copy is herewith submitted of a publication which may be pertinent to the invention as claimed in the above-identified application. The attached form PTO-1449 lists this publication.

The citation of this publication should not be construed as a representation that a thorough search has been made, or that other, more pertinent material is not available.

Respectfully submitted

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## **CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail on the date shown below in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

3/24/2004 Date Name of Person Making Deposit

Page No.: 1 of: 1

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	OTHER DOCUME	NTS (Author (Capitaliz	e), Title, Date, Pag	es, Etc., if kn	own)		
	"Multi-antenna Transceiver Tech	niques for 3G and Beyond	", Ari Hottinen et al., J	ohn Wiley & So	ns, Chichester	, UK, 2003	
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